

FIG. 1A

FIG. 1C

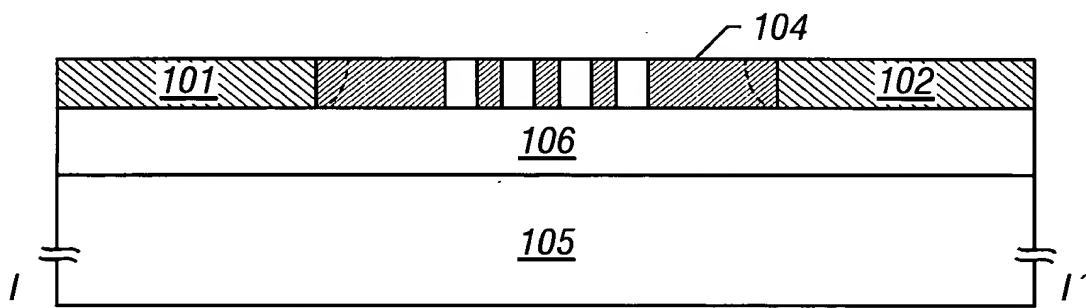


FIG. 1B

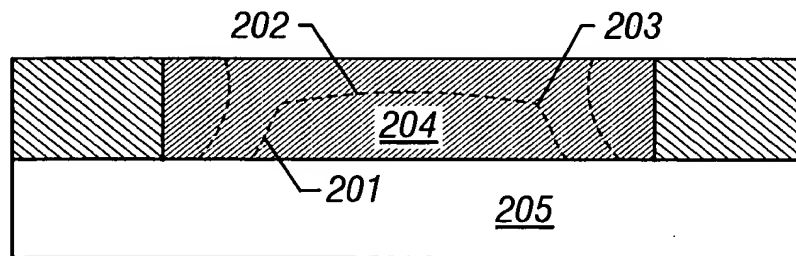


FIG. 2A

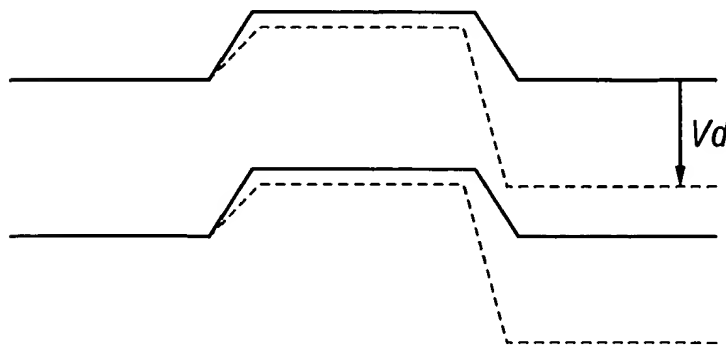


FIG. 2B

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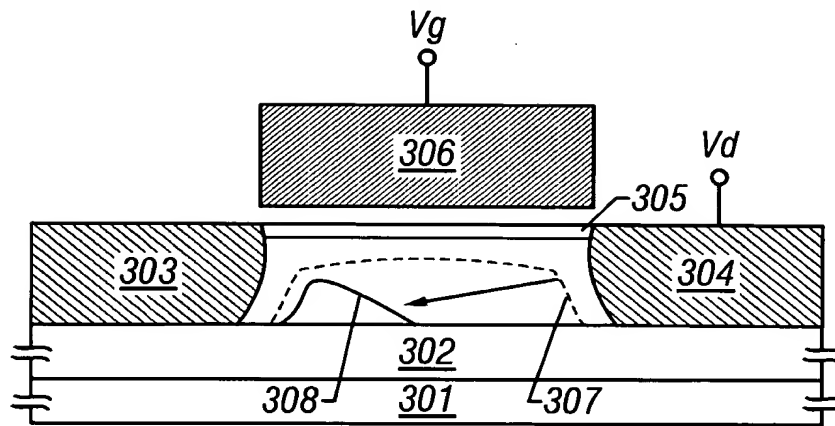


FIG. 3
(Prior Art)

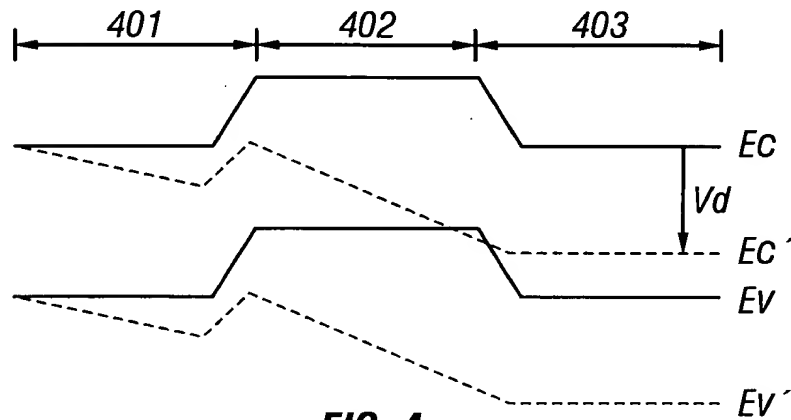


FIG. 4
(Prior Art)

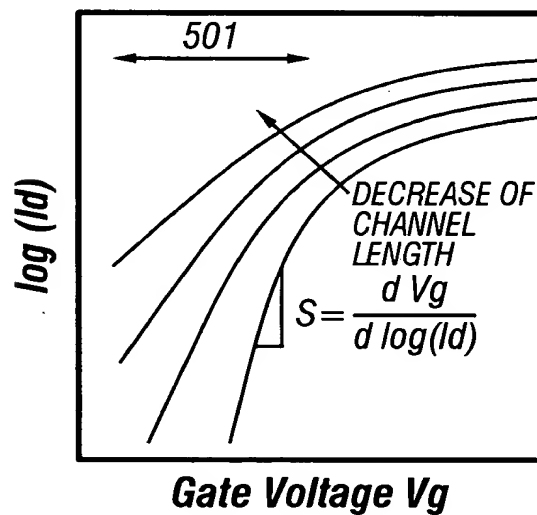


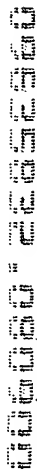
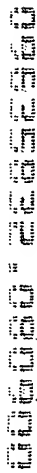
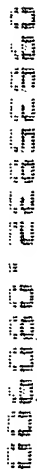
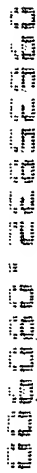
FIG. 5
(Prior Art)

FIG. 6A

FIG. 6B

FIG. 6C

FIG. 6D

[illegible][illegible][illegible][illegible]

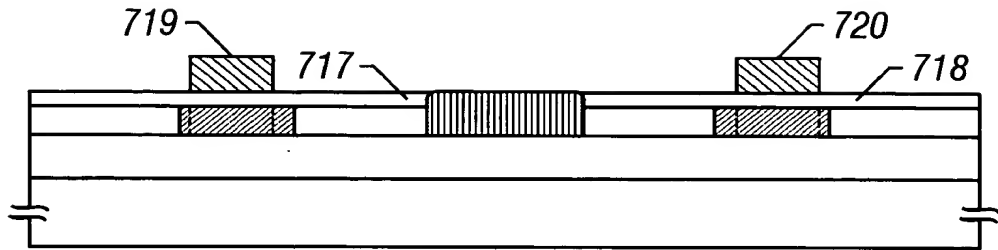


FIG. 8A

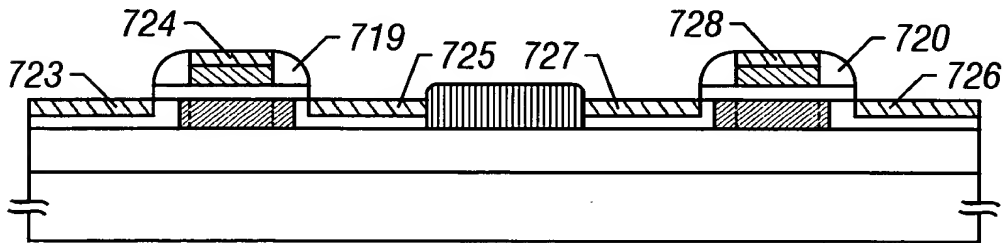
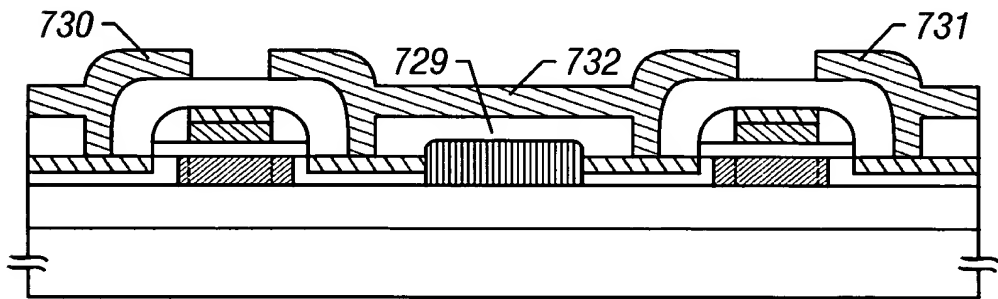


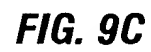
FIG. 8B



N-CHANNEL FET

P-CHANNEL FET

FIG. 8C



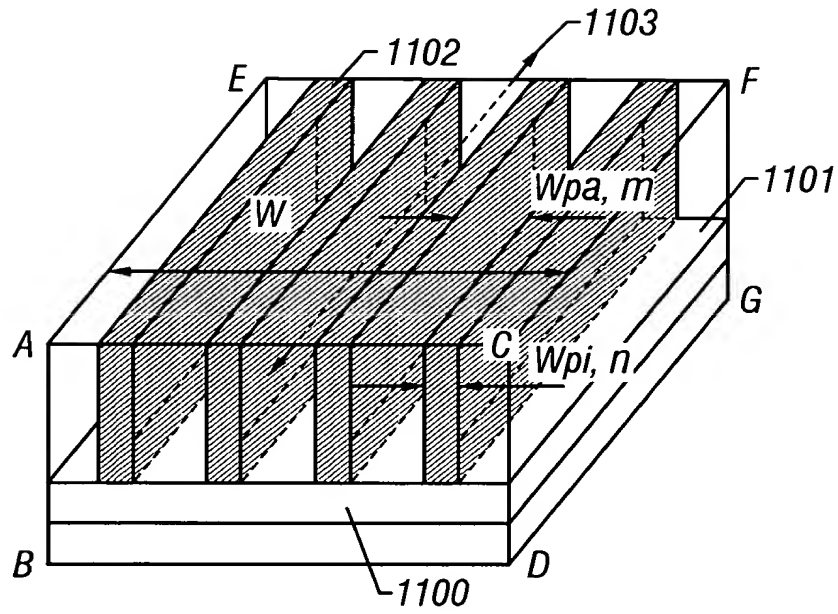


FIG. 11A

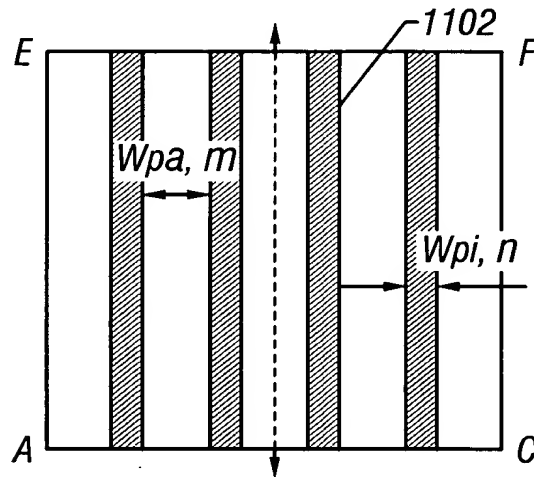


FIG. 11B

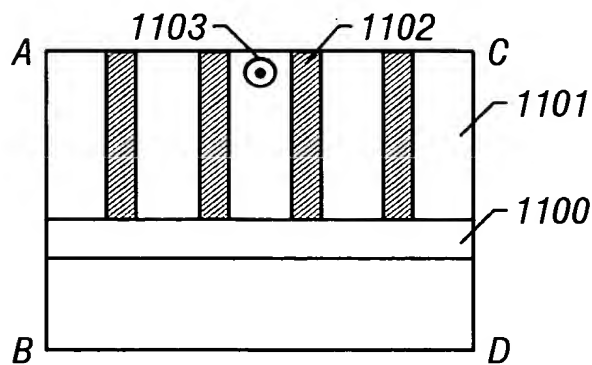


FIG. 11C

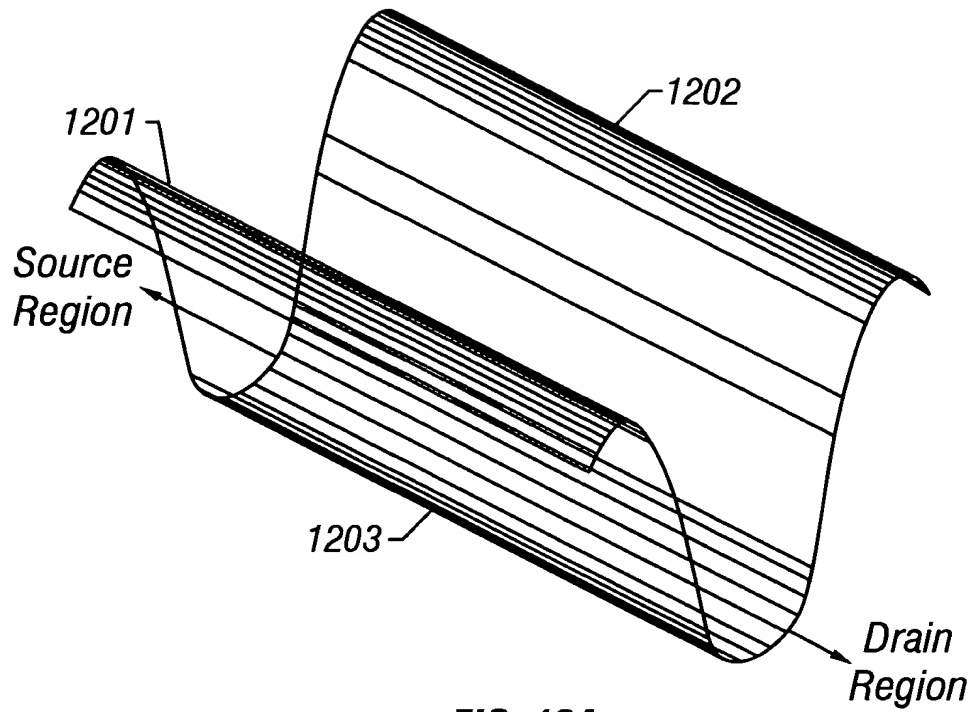


FIG. 12A

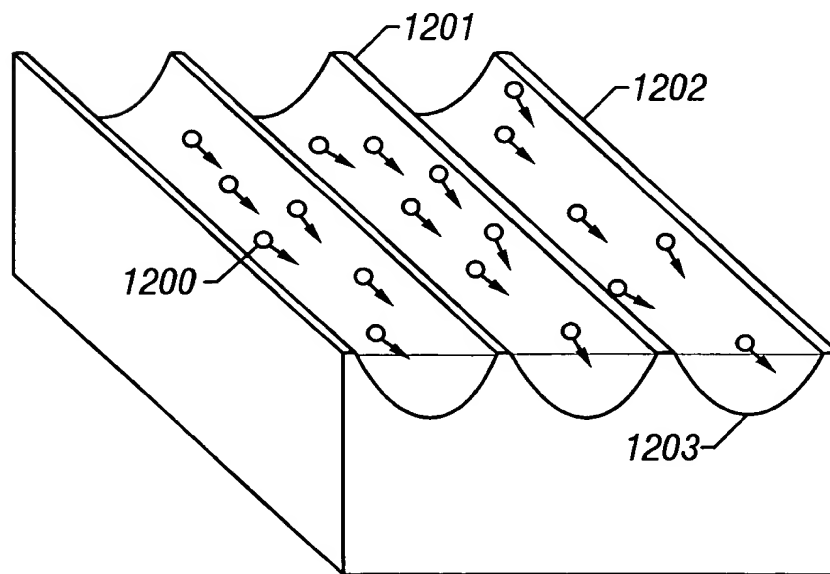


FIG. 12B

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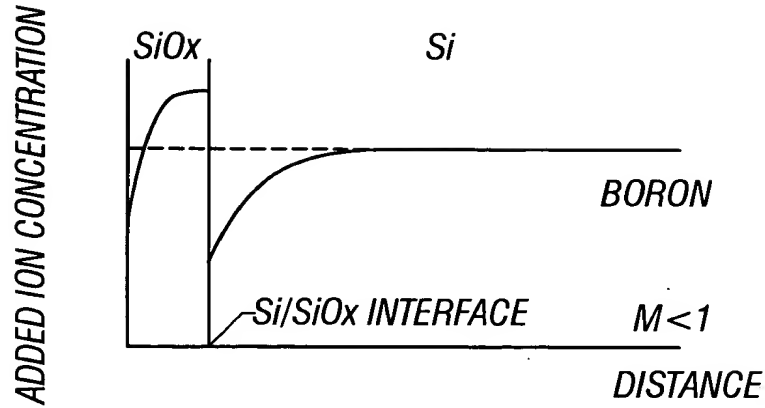


FIG. 13A

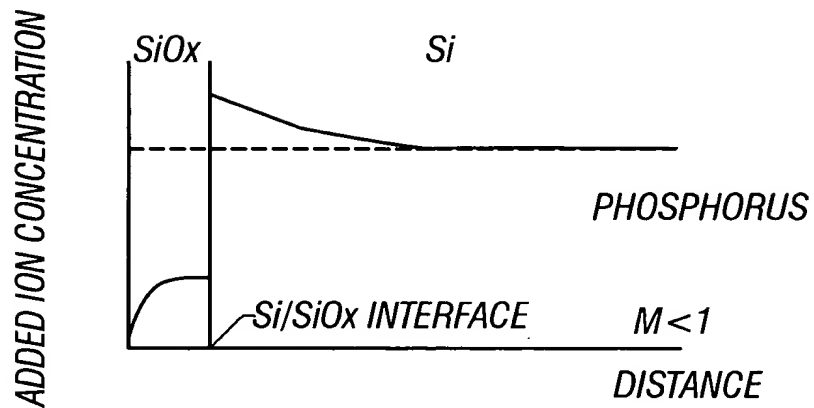


FIG. 13B

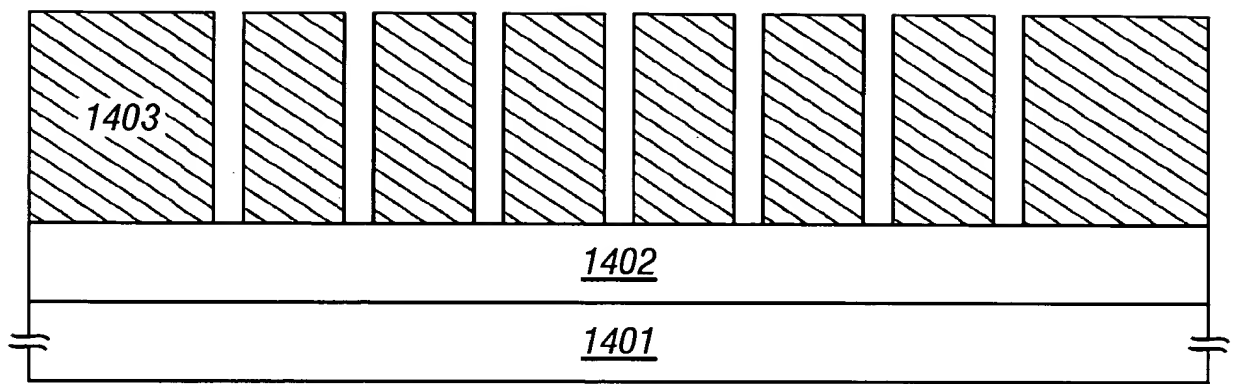


FIG. 14A

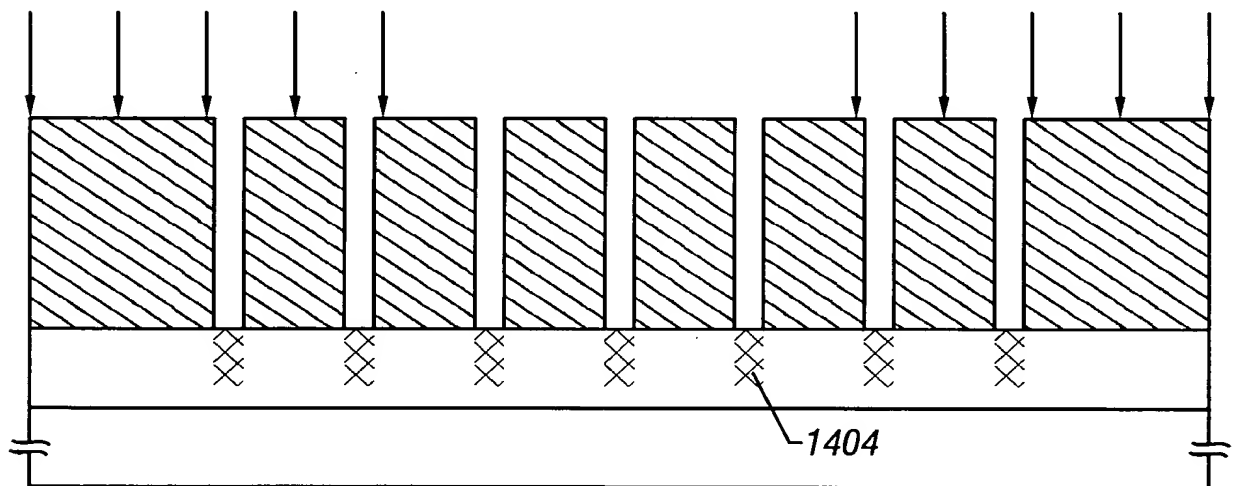


FIG. 14B

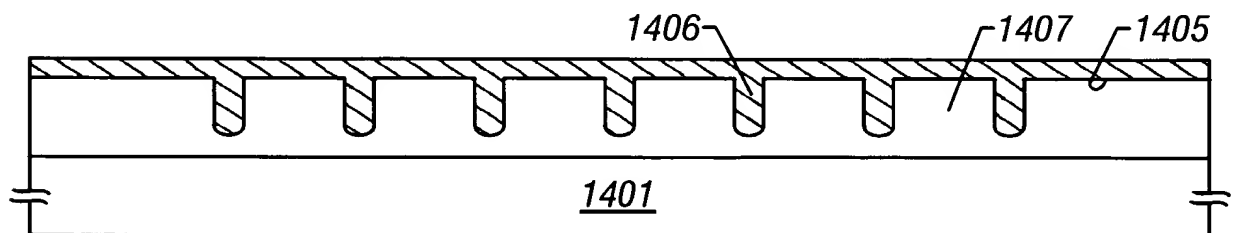


FIG. 14C

00000-22050300

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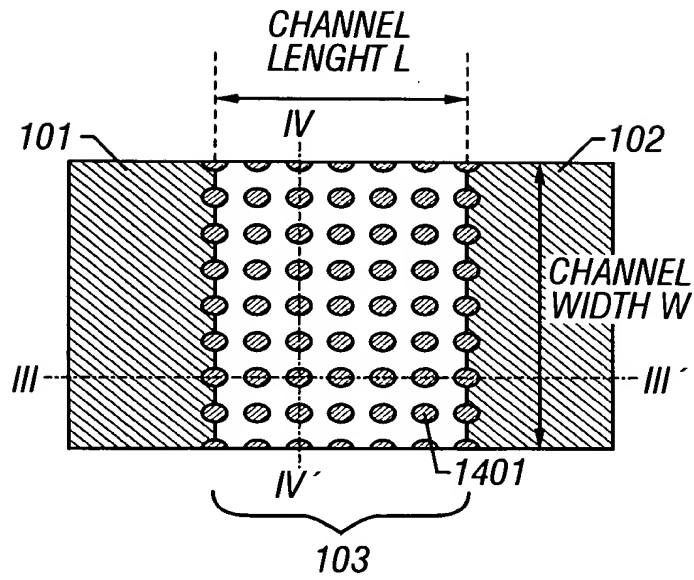


FIG. 15A

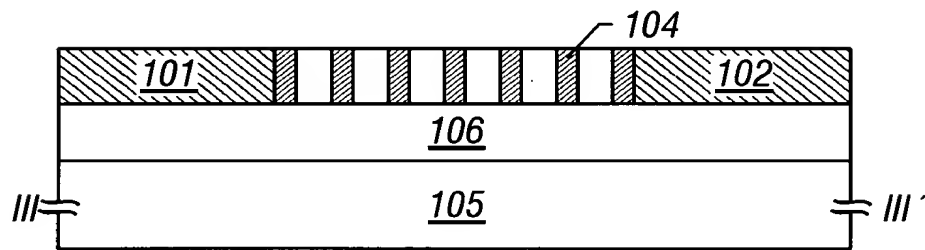


FIG. 15B

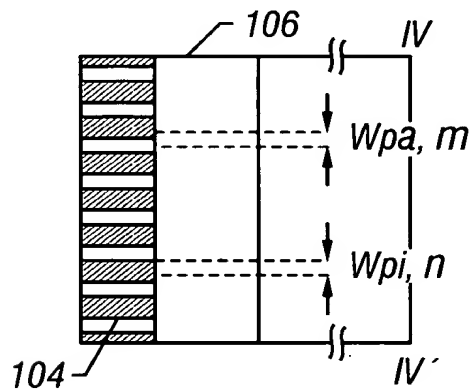


FIG. 15C

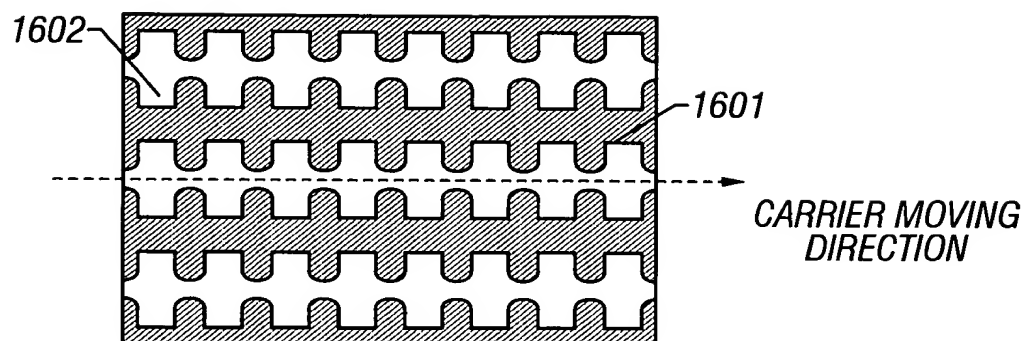


FIG. 16A

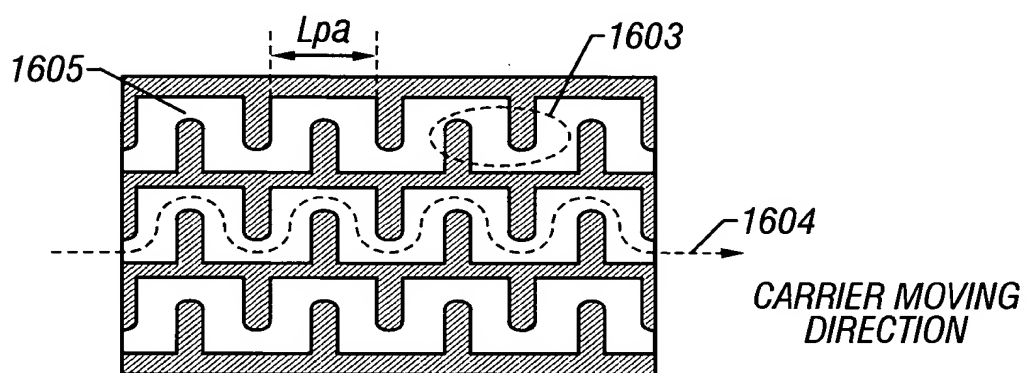


FIG. 16B

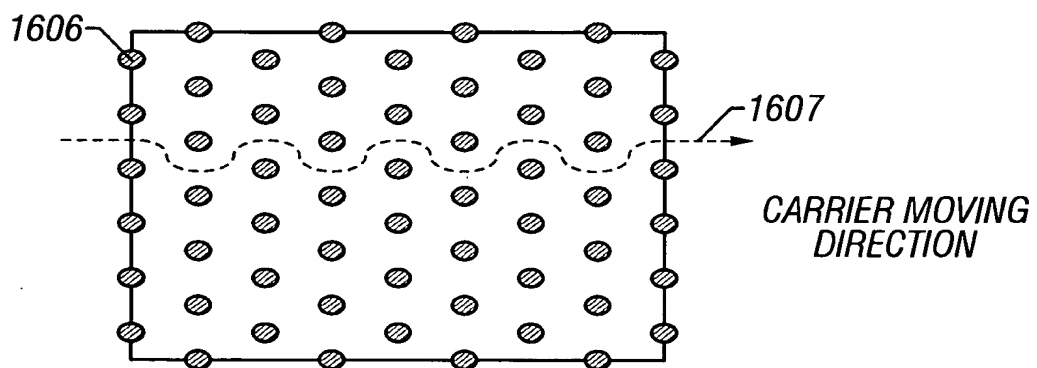


FIG. 16C

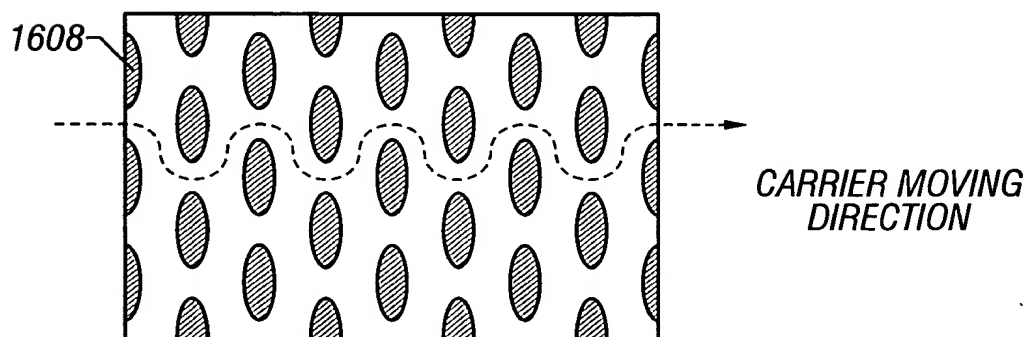


FIG. 16D.

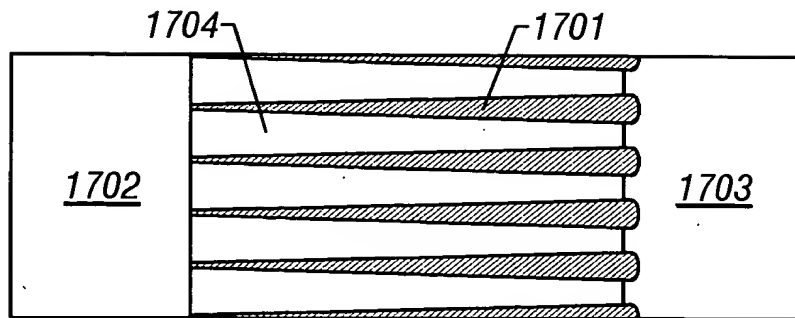


FIG. 17A

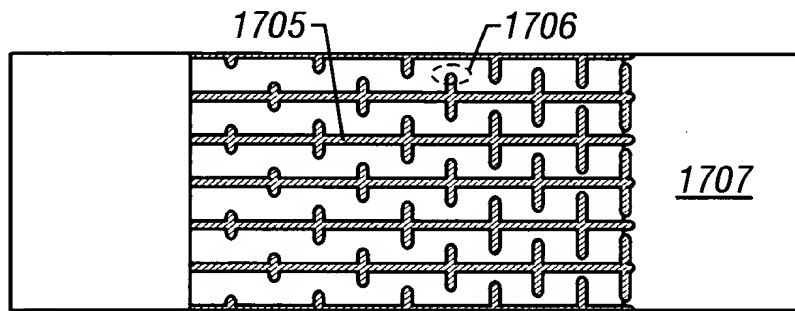


FIG. 17B

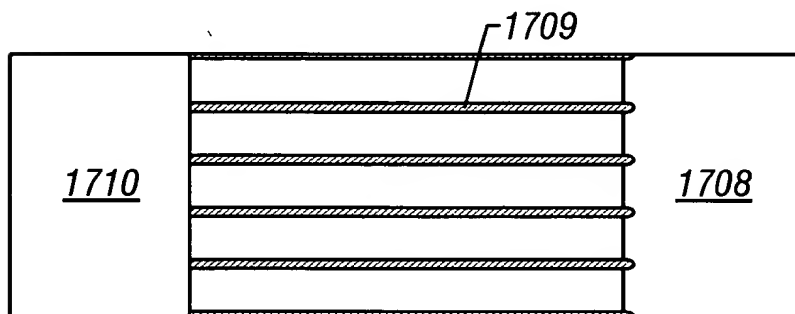


FIG. 17C



FIG. 17D

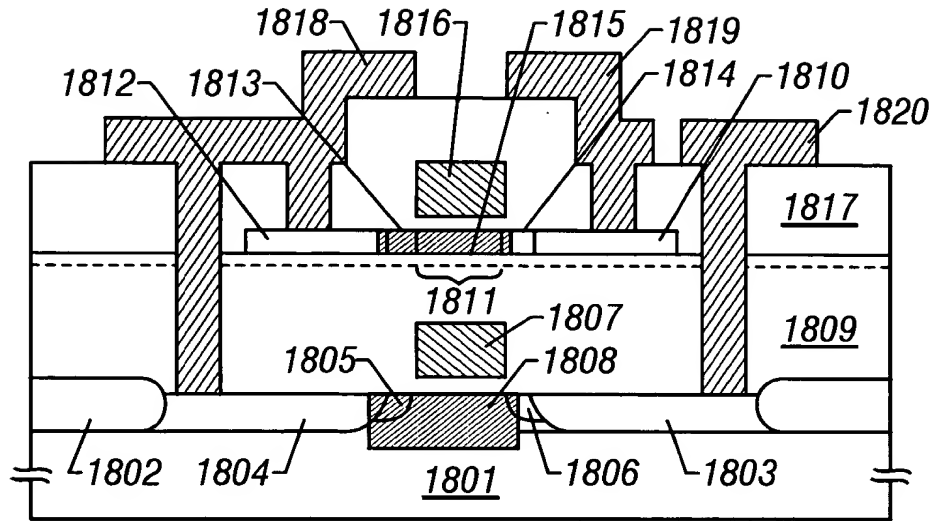


FIG. 18A

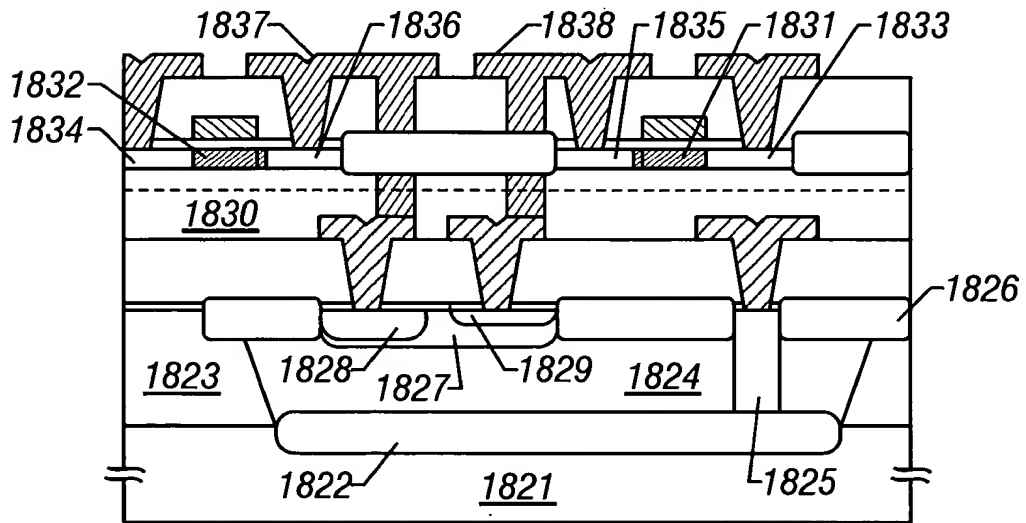


FIG. 18B

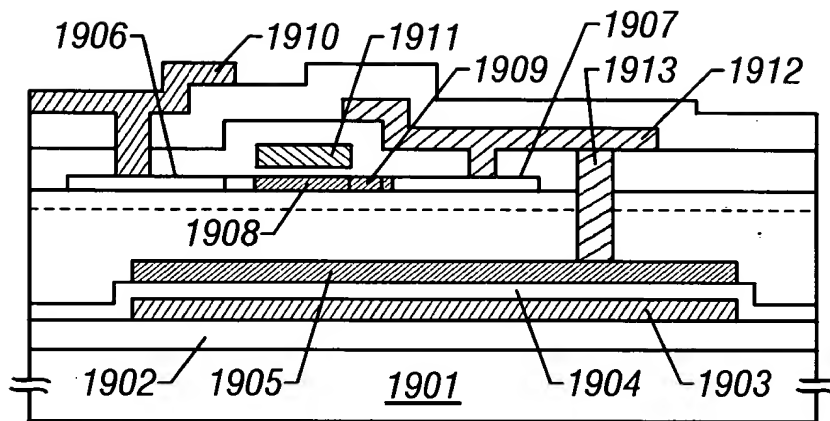


FIG. 19A

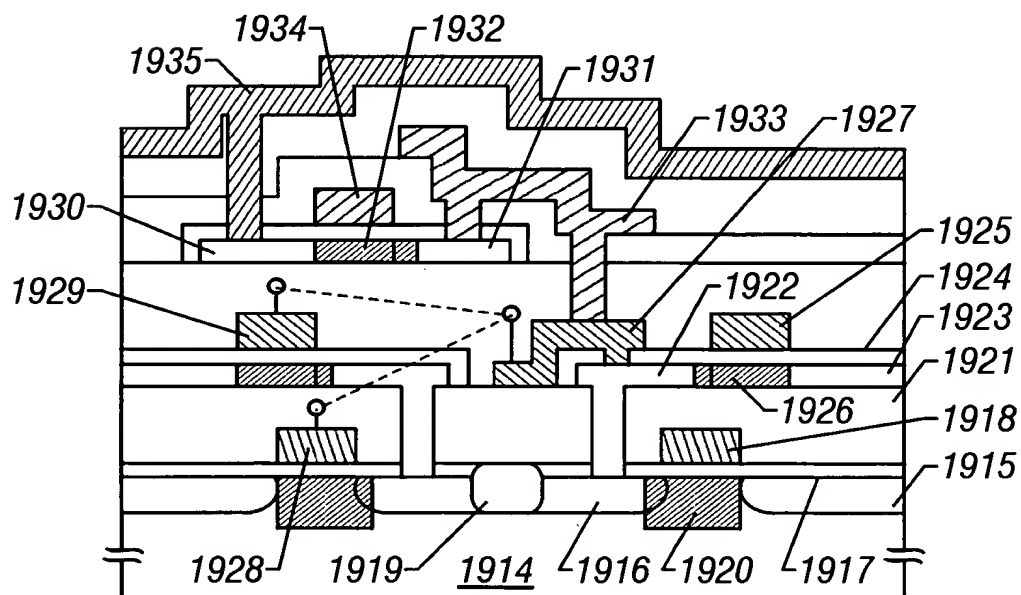


FIG. 19B

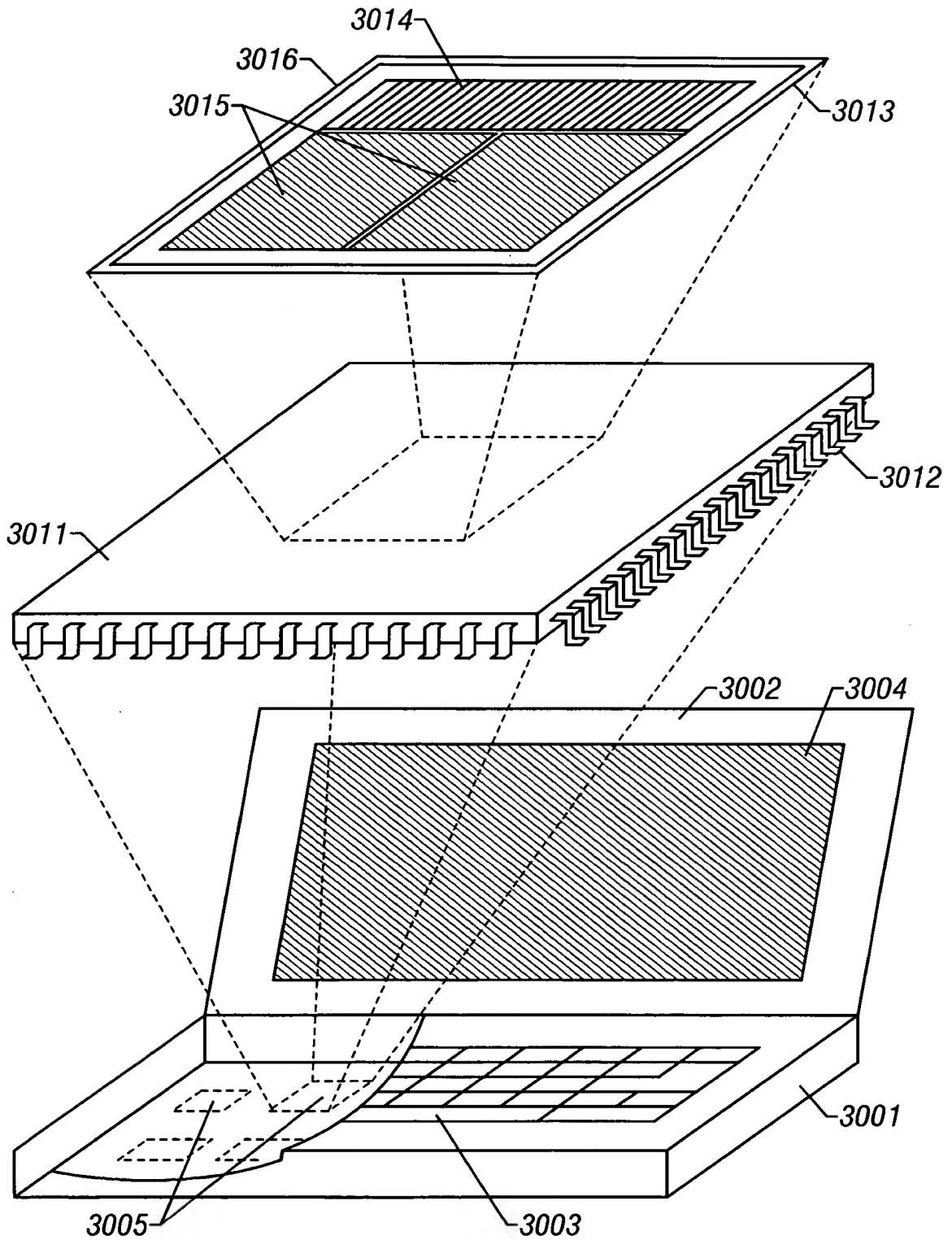


FIG. 20

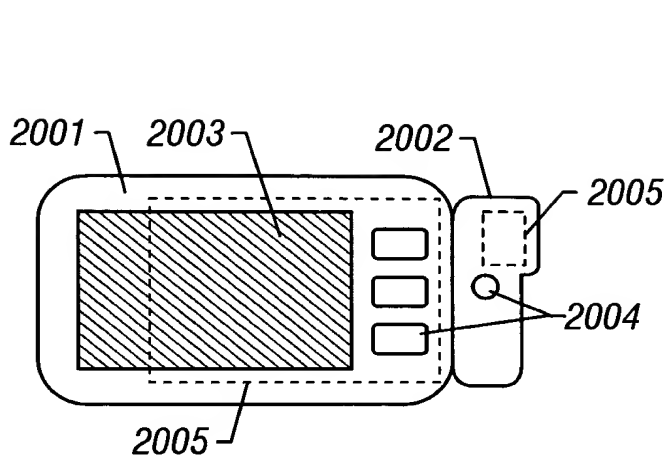


FIG. 21A

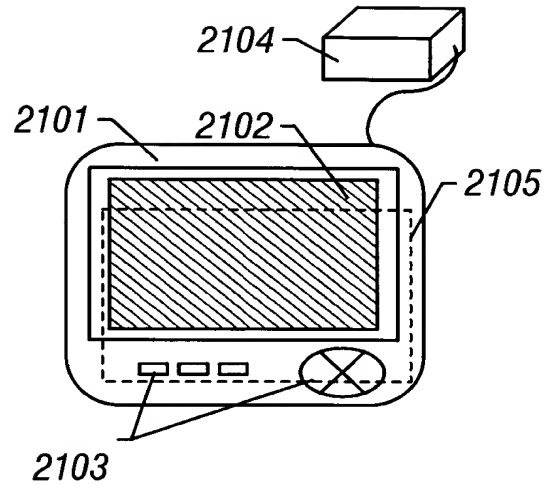


FIG. 21B

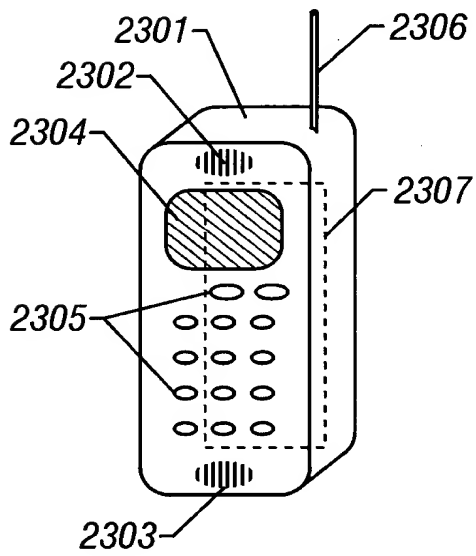


FIG. 21C

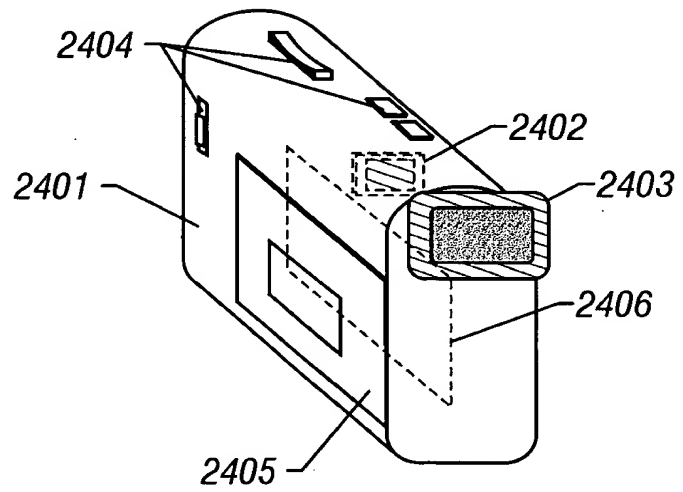


FIG. 21D

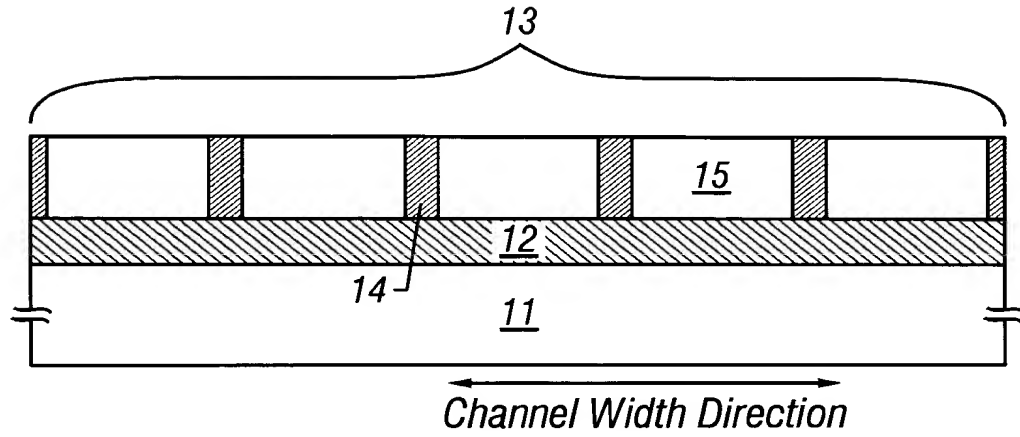


FIG. 22A

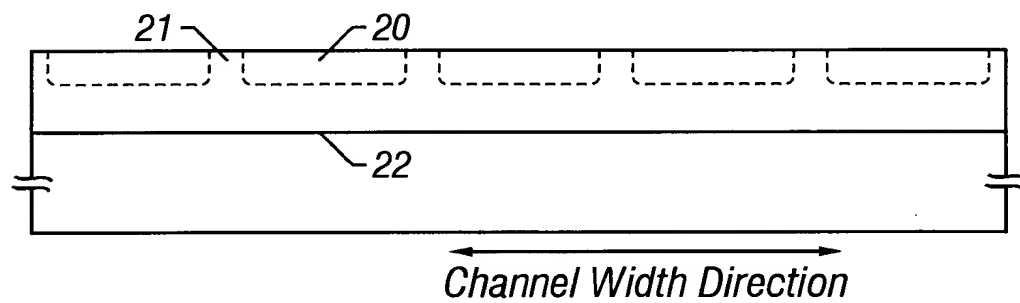


FIG. 22B

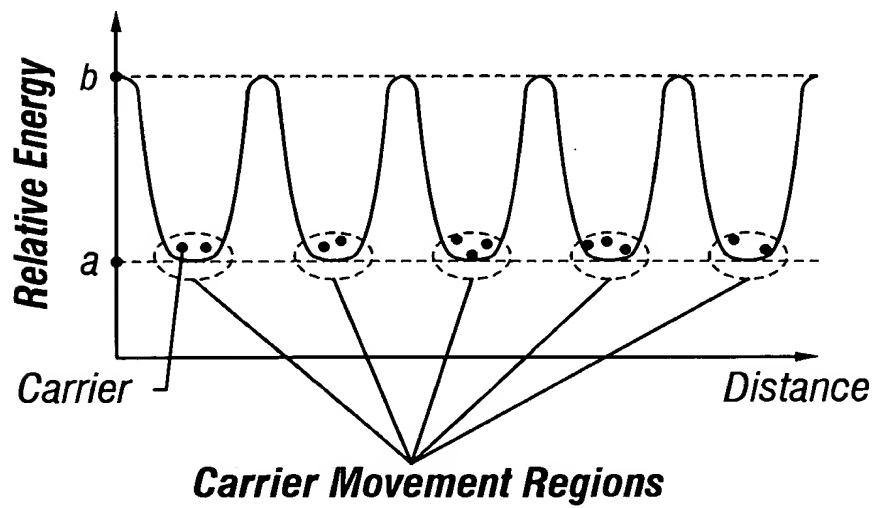


FIG. 22C